

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Chandrakant D. Patel

Confirmation No.:

Application No.: 10/735,467

Examiner:

Filing Date: Dec. 12, 2003

Group Art Unit:

Title: LONGITUDINALLY COOLED ELECTRONIC ASSEMBLY

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97 (c) together with either a:
☐ Statement under 37 CFR 1.97(e), or
☐ a \$180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:
☐ Statement under 37 CFR 1.97(e)(1) or (2), and
☐ a \$180.00 fee set forth in 37 CFR 1.17(p).
(Filed after final action, a notice of allowance, on or before payment of the issue fee)

Please charge to Deposit Account **08-2025** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **08-2025** pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Statement together with any required copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individual(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

☒ I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450. Date of Deposit: March 9, 2004

OR

☐ I hereby certify that this paper is being transmitted to the Patent and Trademark Office facsimile number _____ on _____
Number of pages: _____

Typed Name: John Griecci

Signature: John Griecci

Respectfully submitted,

Chandrakant D. Patel

By John Griecci

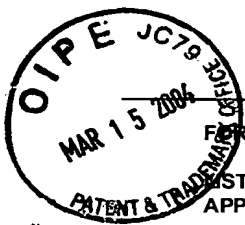
John Griecci

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PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449

OFFICE OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

200209754-1

APPLICATION NO.

10/735,467

CONFIRMATION NO.

APPLICANT

Chandrakant D. Patel

FILING DATE

Dec. 12, 2003

GROUP

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A	4,817,865	Apr. 4, 1989	Wray	
	1B	5,216,579	June 1, 1993	Basara et al.	
	1C	6,134,108	Oct. 17, 2000	Patel et al.	
	1D	6,496,366	Dec. 17, 2002	Coglitore et al.	
	1E	6,525,936	Oct. 31, 2002	Beitelmal et al.	
	1F	6,574,104	Apr. 10, 2003	Patel et al.	
	1G	6,626,300	June 12, 2003	Kaminski et al.	
	1H	6,628,520	Aug. 7, 2003	Patel et al.	
	1I				
	1J				
	1K				

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	1Q	Patel, Chandrakant D., "A Vision of Energy Aware Computing from Chips to Data Centers," ISMME2003-K15, The International Symposium on Micro-Mechanical Engineering, Dec. 1-3, 2003.
	1R	
	1S	

EXAMINER

DATE CONSIDERED